



LPC POST SERIAL BOARD

The LPC POST SERIAL board displays "Port 80" and "Port 81" BIOS and UEFI-BIOS debug code.

This code is displayed on 4x 7-segment display, and also send over a serial connection to a Host PC, to be able to record the complete Port 80/81 sequence.

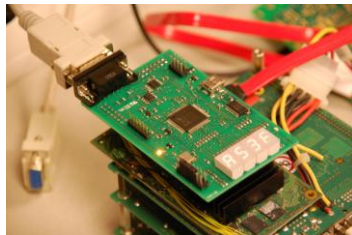
By a jumper setting the serial connection can be changed from forwarding the port 80/81 sequence to a UART/COM port.

Versions of LPC POST SERIAL:

- The 2x7way socket fits onto the standard "TPM"/"LPC" header (0.1"pitch) located on the target PC board.
- A similar 2x7 way socket, pin-out of this connector defined by Intel Server division

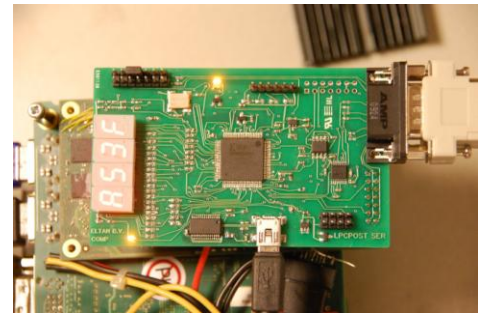
Applications

- Debugging x86 PC board
- PC computer repair tool
- Functional x86 board test fixtures tool



Jumper settings

- Selection of output of port 80/81 code to serial interface or UART output
- Rotate 4x 7 segment display

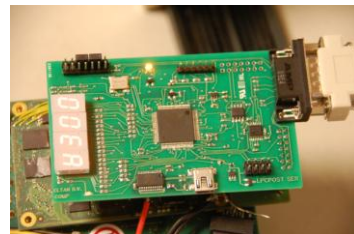


Mechanical dimensions

Board size: 94.5 mm x 58 mm

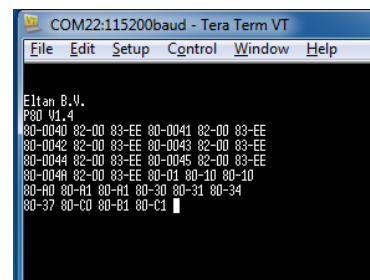
Operating temperature

0 °C – 70 °C



User Manual

The LPC POST SERIAL user manual includes: connector pin definitions, schematics, info of Phoenix® and Award™ BIOS code table, Xilinx re-programming instructions, and Test measurement assignment test point



Order information

S00 LPC POST SERIAL TPM HEADER
S01 LPC POST SERIAL INTEL HEADER
Or contact ELTAN about your requirements

Features

(UEFI) BIOS debug code display
Port 80 and Port 81 display

Serial out of port 80/81 code or
(set jumper) use serial out as
UART function