E L T A N

LPC POST SERIAL BOARD

Features

(UEFI) BIOS debug code display
Port 80 and Port 81 display

Phoenix® BIOS debug code, Intel® FSP debug code, AMD firmware initialization code, UEFI debug code, Coreboot™ Open Source firmware debug code, etc.

Serial out of:
Port 80/81 code and Port 82/83
Byte display and word display format

Or (set jumper) and use serial out as UART function: COM1

Use jumper to select between COM1/COM2

Serial PORT:
Sub D 9 pins connector and USB COM port terminal

USB powered:
Display of power down sequence after power-off target

Time stamp option for boot time analysis

The LPC POST SERIAL board displays "Port 80" and "Port 81" BIOS and UEFI-BIOS debug code.

This code is displayed on 4x 7-segment display, and also send over a serial connection to a Host PC, to be able to record the complete Port 80/81/82/83 sequence.

By a jumper setting the serial connection can be changed from forwarding the port 80/81 sequence to a UART/COM port.

Versions of LPC POST SERIAL:
- The 2x8 way socket fits onto the standard "TPM"/"LPC" header (0.1"pitch) located on the target PC board.
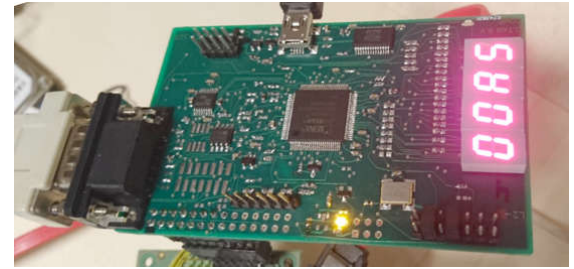- Option other connector

Applications
- Debugging x86 PC board
- PC computer repair tool
- Functional x86 board test fixtures tool

Jumper settings
- Selection of output of port 80/81 code to serial interface or UART output
- Rotate 4x 7 segment display
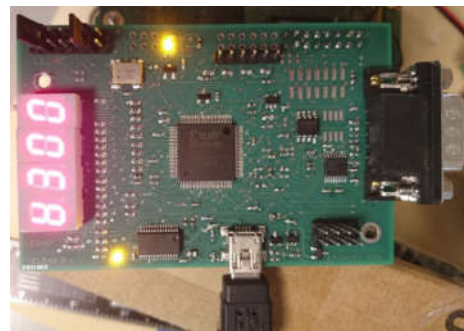- Time Stamp option
- COM1 or COM2 port

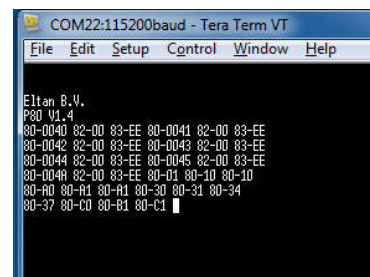**Mechanical dimensions L3**
Board size: 89 mm x 58 mm

Operating temperature
0 °C – 70 °C

User Manual
The LPC POST SERIAL user manual includes: connector pin definitions, schematics, info of Phoenix® and Award™ BIOS code table, optional FPGA re-programming instructions, and Test measurement assignment test points

Order information
LPCPOST SER L3 S00
Or contact ELTAN about your requirements

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