

## LPC POST

The LPC POST displays Port 80 BIOS debug code. The adapter fits into a 32 pins PLCC socket of the Firmware Hub (FWH) located on a PC motherboard. Low Pin Count (LPC) interface is also available on separate header

## Applications

- Port 80 code display via standard available FWH - 32 pins PLCC socket - interface
- Debugging x86 PC computer board
- PC computer repair tool
- In system Firmware Hub BIOS programming
- Functional x86 board fixtures test tool



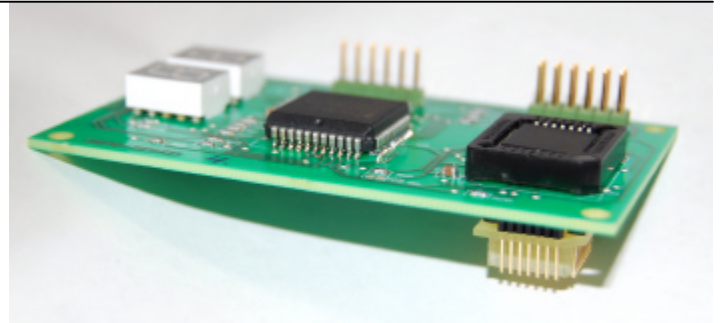
## Usage instructions

- Insert 32 PLCC adapter into the 32 PLCC socket of the FWH on the main board
- Power up system and BIOS Port 80 code on double seven segment is displayed
- The adapter is designed to use in ESD protected laboratory environment or use inside a chassis



## Power consumption

- Low power consumption on +3.3 Volt
- 2 seven segment display components
- Xilinx EPLD and Firmware Hub



## Mechanical dimensions

Board size: 85 mm x 49 mm

## Operating temperature

0 °C – 50 °C

## Multiple versions of LPC POST

The standard LPC POST version is equipped with the 32 pins PLCC adapter, capable to fit into 32 pins PLCC Firmware Hub socket. An alternative address can be selected to display Port 90 code.

Other versions of the LPC are also available: Female LPC header, capable to fit on commonly used LPC TPM expansion header. The LPC POST header also supports: “selectable Firmware Hub boot device” which gives the ability to select between FWH on board and on LPC POST. Special configurations and custom design is available on request.

## User Manual

The LPC POST user manual includes: connector pin definitions, schematics, Phoenix® and Award™ BIOS code table, Xilinx re-programming instructions, and Test measurement assignment test point

## Ordering information

Contact ELTAN about your requirements



## ELTAN BV

Het Schild 13  
The Netherlands  
www.eltan.com

5275 EB Den Dungen  
+31 (0) 73 – 5944660  
info@eltan.com